



PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Group Art Unit: 2822

Yashuhiko TAKEMURA et al. ✓

Examiner: Yung A. Lin

Serial No.: 09/321,715 ✓

Filed: May 28, 1999 ✓

For: SEMICONDUCTOR DEVICE
AND PROCESS FOR
FABRICATING THE SAME)

CERTIFICATE OF MAILING

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Deborah J. Somme

#6 (DS)
J. Maill
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**INFORMATION DISCLOSURE STATEMENT AND
NOTIFICATION OF RELATED**

Assistant Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

Sir:

In accordance with the provisions of 37 C.F.R. §1.56 and 37 C.F.R. §1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

U.S. Patent Nos. 5,091,334, 5,262,350, 5,591,987 and 5,859,443 discuss separating active layers of transistors by thermal oxidation.

U.S. Patent Nos. 5,543,352, 5,585,291, 5,612,250, 5,705,829, 5,882,960 and 5,895,933 discuss oxidizing a semiconductor film at a pressure of 1 atm or more.

The attention of the Examiner is directed to related application Serial Nos. 08/104,264 (our ref. 0756-776) filed September 8, 1992; 08/371,486 (our ref. 0756-1172) filed January 11, 1995; 08/521,532 (our ref. 0756-1404) filed August 30, 1995; 08/975,918 (our ref. 0756-1758) filed November 21, 1997; and 09/222,185 (our ref. 0756-1686) filed June 3, 1997.


Serial Nos. 08/104,264 and 08/371,486 discuss separating active layers of transistors by thermal oxidation.

Serial Nos. 08/521,532, 08/975,918 and 09/222,185 discuss the oxidation at a higher pressure.

Additionally, the article submitted herewith by Stanley Wolf, Ph.D et al., discusses thermal oxidation of single crystal silicon.

Respectfully submitted,

NIXON PEABODY LLP


Eric J. Robinson
Registration No. 38,285

EJR:BCO/dms
Attorney Docket No.: 0756-1974
Sixbey, Friedman, Leedom & Ferguson, P.C.
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
Telephone (703) 790-9110

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